

| | Type | L # | Hits | Search Text | DBs | Time Stamp | Comments |
|----|------|-----|--------|--|----------------------------|------------------|----------|
| 1 | BRS | L1 | 98566 | power near5 (state or mode) | USPA T; US-P GPUB | 2004/02/10 19:02 | |
| 2 | BRS | L2 | 32862 | 1 and logic | USPA T; US-P GPUB | 2004/02/10 19:03 | |
| 3 | BRS | L3 | 440 | deep adj sleep | USPA T; US-P GPUB | 2004/02/10 19:03 | |
| 4 | BRS | L4 | 111 | 2 and 3 | USPA T; US-P GPUB | 2004/02/10 19:03 | |
| 5 | BRS | L5 | 56511 | transition\$3 near5 (state or mode) | USPA T; US-P GPUB | 2004/02/10 19:04 | |
| 6 | BRS | L6 | 65 | 4 and 5 | USPA T; US-P GPUB | 2004/02/10 19:04 | |
| 7 | BRS | L7 | 113496 | (frequency or speed) near5 voltage | USPA T; US-P GPUB | 2004/02/10 19:05 | |
| 8 | BRS | L8 | 25 | 6 and 7 | USPA T; US-P GPUB | 2004/02/10 19:45 | |
| 9 | BRS | L9 | 883 | north adj2 bridge | USPA T; US-P GPUB | 2004/02/10 19:46 | |
| 10 | BRS | L10 | 785 | south adj2 bridge | USPA T; US-P GPUB | 2004/02/10 19:46 | |
| 11 | BRS | L11 | 1 | 8 and (9 or 10) | USPA T; US-P GPUB | 2004/02/10 19:57 | |
| 12 | BRS | L12 | 818 | interfac\$3 near5 (processor or microprocessor) near5 bridge | USPA T; US-P GPUB | 2004/02/10 19:58 | |
| 13 | BRS | L13 | 656 | 9 and 10 | USPA T; US-P GPUB | 2004/02/10 19:58 | |

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|----|------|-----|------|-------------|----------------------------|----------------------|----------|
| 14 | BRS | L14 | 83 | 12 and 13 | USPA T; US-P GPUB | 2004/02/1 0 19:58 | |
| 15 | BRS | L15 | 24 | 1 and 14 | USPA T; US-P GPUB | 2004/02/1 0 19:58 | |